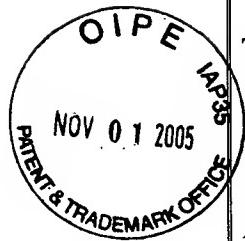


**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Onchuen (Daryn) Lau, et al  
Assignee: IDT (was ZettaCom)  
Title: Variably Delayable Transmission of Packets Between Independently Clocked Source, Intermediate, and Destination Circuits While Maintaining Orderly and Timely Processing in One or Both of the Intermediate and Destination Circuits  
Application No.: 09/997,507 Filing Date: 11/26/2001  
Examiner: Ali, Syed J. Group Art Unit: 2195  
Docket No.: Zetta-1004us0 [idt-1962] Confirm 7256



San Jose, California  
October 28, 2005

**COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450**

**AMENDMENT**

Dear Sir:

In response to the Office Action having a mailing date of August 5, 2005, please amend the application as set forth below.

**Claims Listing** begins on page 2 of this paper.

**Amendment to Specification** begin on page 16 of this paper.

**Remarks** begin on page 17 of this paper.

MacPherson Kwok Chen & Heid  
LLP  
1762 Technology Drive,  
Suite 226  
San Jose, CA 95110  
Telephone: (408) 392-9250  
Facsimile: (408) 392-9262

11/02/2005 HTECKLU1 00000016 502257 09997507

01 FC:1201 600.00 DA  
02 FC:1202 450.00 DA